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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/731,217

12/09/2003

Antonio Rinaldi

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EXAMINER

HASAN, SYED Y

ART UNIT

PAPER NUMBER

2621

MAIL DATE

DELIVERY MODE

04/30/2008

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/731,217	Applicant(s) RINALDI, ANTONIO	
	Examiner SYED Y. HASAN	Art Unit 2621	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 2/4/2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 11 - 20 is/are pending in the application.
- 4a) Of the above claim(s) 15 -16 and 19-20 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 11 - 14 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>6/25/2004 and 8/9/2004</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Restriction/Election

1. Applicant's election of invention I (claims 11 – 14) in the reply filed on February 14, 2008 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).

DETAILED ACTION

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

3. Claims 11 – 14 are rejected under 35 U.S.C. 102(a) as being anticipated by Berman et al (US 5404172)

Regarding **claim 11** Berman et al discloses a copy protection pulse detection circuit, comprising:

a pulse detector (fig 10, col 16, line 37 – 52, pulse detection) wherein the pulse detector receives an input video signal (fig 2, node 22, col 6, line 53 - 63) and sets a pulse detect indication each time a pulse that exceeds a first threshold is detected in the input video signal (fig 10, col 16, line 61 to col 18, line 34, illustrates pulse detection and threshold detection)

an accumulator operably coupled to the pulse detector (fig 10, col 16, line 37 –

52, accumulator) wherein the accumulator counts a number of pulses received in each frame of the input video signal to produce an accumulated count (fig 10, col 16, line 61 to col 18, line 34, illustrates accumulator and pulse counting) a field pulse comparator operably coupled to the accumulator, wherein the field pulse comparator compares the accumulated count for each field with a second threshold, wherein when the accumulated count exceeds the second threshold the field; for a field, the field pulse comparator asserts a positive count indication for (col 17, line 9 – 42, illustrates the various counts for threshold checking in order to obtain a positive count indication)

a decision counter operably coupled to the field pulse comparator, wherein the decision counter increments a decision count when the positive count indication is asserted for a field and decrements the decision count when the positive count indication is not asserted for a field (col 17, line 9 – 66, illustrates the effect of positive count) and

a decision comparator operably coupled to the decision counter, wherein when the decision count reaches a high threshold, the decision comparator sets a pulses detected indication, wherein when the decision count reaches a low threshold, the decision comparator clears the pulses detected indication (col 15, line 66 to col 16, line 24, illustrate the function of the comparator by sourcing or sinking current)

Regarding **claim 12** Berman et al discloses the copy protection pulse detection circuit, wherein the pulse detector further comprises:

an analog to digital converter that converts the input video signal to a digital input signal (fig 7d, ADC, col 10, lines 46 – 62)

a low pass filter operably coupled to the analog to digital converter, wherein the low pass filter filters the digital input signal to produce a filtered input signal (fig 7d, 7d4 illustrates a capacitor to ground which is a low pass filter to provide filtered signal) and

a pulse detection comparator operably coupled to the low pass filter (fig 7d) wherein the pulse detection comparator compares digital values in the filtered input signal with the first threshold, wherein the pulse detection comparator sets the pulse detect indication each time a digital value exceeds the first threshold (col 7, line 31 to col 8, line 5, illustrates the function of the comparator in providing a threshold)

Regarding **claim 13** Berman et al discloses the copy protection pulse detection circuit, wherein the pulse detector further comprises a blanking interval gate, operably coupled to the pulse detection comparator, wherein the blanking interval gate selectively passes the pulse detect indication based on a received signal indicating that the input video signal is in the vertical blanking interval (col 14, lines 45 – 66, illustrate blanking interval gate and its function)

Regarding **claim 14** Berman et al discloses the copy protection pulse detection circuit, wherein the detection circuit is included on a video graphics integrated circuit (fig 2, col 11, lines 11 – 36, illustrate an integrated circuit chip)

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure

Bermen et al (US 5596372) discloses a video signal data and composite synchronization extraction circuit for on-screen display

Jordan (US 5760844) discloses a video signal data and composite synchronization extraction circuit for on-screen display

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Syed Y. Hasan whose telephone number is 571-270-1082. The examiner can normally be reached on 9/8/5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thai Tran can be reached on 571-272-7382. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

S. Y. H.
04/16/2008

/Thai Tran/
Supervisory Patent Examiner, Art Unit 2621